



Attorney's Docket No.: 07977-175002 / US3397D1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2815
Serial No.: 09/736,724 Examiner : Paul E. Brock
Filed : December 13, 2000
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	73	-	73	=	0	\$0
Independent	16	-	16	=	0	\$0

Applicant hereby petitions under 37 C.F.R. §1.136
for a 1 month extension of time. \$110

TOTAL FEE DUE \$110

A check for \$110 is attached.

Respectfully submitted,

/BY
DWIGHT THOMPSON
REG. NO. 53,688

Date: May 27, 2003

Dwight U. Thompson
Dwight U. Thompson
Reg. No. 53,688

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
deposited with the United States Postal Service as first class mail with
sufficient postage on the date indicated below and is addressed to the
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

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Tobi Terry

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06/04/2003 JBALIAN 00000058 09736724

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